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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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**APPLICANTS: Mitani Shinichiro Ikeda Takahide Mori Kazutaka Higo Hiroyuki					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09 554 754 05/19/2000 US# 63 922 77 5/21/02					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 9-320373 11/21/1997					
PG. PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claim: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				ATTORNEY DOCKET NO 501 38435VX1	
Verified and Acknowledged by Examiner: <input checked="" type="checkbox"/>					
TITLE : Semiconductor device					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due Date Paid		Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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